

CS486C – Senior Capstone Design in Computer Science

Project Description

Project Title: AI-ACETONE (AI-Automated CounterExample Test Objectives by Negating Expectations)



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Project Overview:

At SanDisk, we test and validate our popular storage products to mark our seal of approval on the quality we uphold. In directed testing, we focus on specific, targeted features to verify whether they behave according to the design. We are checking whether the Product Team has implemented the intended functionality and whether it meets certain requirements—referred to as *properties* in TLA+ (Temporal Logic of Actions).

TLA+ is a mathematical language used for formally specifying system designs. It comes with tools such as TLC, the TLA+ model checker, which exhaustively explores the state space of a design.

We recognize that our Test Engineers cannot anticipate all possible combinations of test sequences. However, we aim to leverage the exhaustive exploration capabilities of TLC to verify that a TLA+ specification satisfies its defined properties. If a property is violated, TLC provides an error trace from the initial state, showing the shortest steps that lead to the violating state. This trace is known as a *counter-example*.

A TLA+ specification should only proceed in our development process if it passes TLC model checking. Otherwise, it would be logically unsound to develop a design that we know fails to meet its requirements. However, due to potential interpretation gaps between conceptual design and the development of the physical product, we must formulate *Test Objectives* during the design phase. These objectives provide a proactive set of ideas of test sequences to apply once the product is developed.

Formulating Test Objectives requires a deep understanding of the properties and how the product design satisfies them. This process is intricate and prone to gaps—known as *test holes*—which result from inadequate testing. Since a TLA+ specification captures all possible behaviors of the product design in relation to its requirement properties, we can systematically **negate each expected** property. TLC will then generate **counterexamples**, which effectively become our **Test Objectives**.

The proposal is to **automate** this process through Agentic **AI** in VSCode IDE, which would offer the following benefits:

1. May provide test sequences that cover **intricate corner cases that a Test Engineer might not have anticipated** or included as directed tests in the Test Objectives.
2. Provides an **automated list of Test Objectives** that are portable to SanDisk Design Document template, which is highly convenient for Test Engineers.

